

The Mathematics Genealogy Project - Daesun Oh -

Daesun Oh . MathSciNet. Low Complexity VLSI Architectures for LDPC Decoders. Mathematics Subject Classification: 94 Information and communication, circuits .

<http://genealogy.math.ndsu.nodak.edu/id.php?id=157355>

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FPGA Implementation of Low Complexity VLSI -

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