

High Speed Digital Design: Design Of High Speed Interconnects And Signaling By Steven Krooswyk

By Steven Krooswyk

Pearson - High Speed Digital Design: A Handbook of -

High Speed Digital Design: A Handbook of Black Magic Howard Johnson,

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Structure preserving reduction of -

which arises from frequency-dependent effects in high speed interconnects, for high-speed digital by high-level design automation tools

http://dl.acm.org/ft_gateway.cfm?id=1065824&type=pdf

Signal integrity - Wikipedia, the free -

Signal integrity or SI is a set of measures of the quality of an electrical signal. In digital electronics, High speed digital system design :

http://en.wikipedia.org/wiki/Signal_integrity

High- Speed Digital Test Interfaces - Springer -

High-Speed Digital Test Interfaces High-Speed Digital Design, Prentice Hall, 1993. [5] Howard Differential Signaling Doesn t Require Differential

http://link.springer.com/chapter/10.1007/0-387-29409-0_5

Mousetrap: High- speed transition- signaling -

High-speed transition-signaling digital design technology scales to date remarkably satisfying the Moore s Law. The circuits became denser with the scaling

<http://citeseerx.ist.psu.edu/showciting?cid=9408891>

Syllabus - Scribd -

M.E Vlsi Design Regulation2013 Syllabus. M.E Advanced Signal Integrity for High-Speed Digital High Level Synthesis. Logic and Interconnect design

<https://www.scribd.com/doc/206739758/Syllabus>

High Speed Digital Design - Hanqiao Zhang - Bok -

High Speed Digital Design discusses the major factors to consider in designing a high speed digital system and how design concepts affect the functionality of the

<http://www.bokus.com/bok/9780124186637/high-speed-digital-design/>

Howard Heck | LinkedIn -

applies them to high-speed digital design creating Ultra-High Speed Digital Interconnects signaling analysis.

Authors: Howard Heck, Tao

<https://www.linkedin.com/in/howardheck>

High-Speed Digital System Design A Handbook of -

High-Speed Digital System Design A Handbook of Interconnect Theory and Design Practices. Uploaded by Ali Moulaei Nejad. 1 of 2:

<http://www.academia.edu/1886218/High-Speed-Digital-System-Design-A-Handbook-of-Interconnect-Theory-and-Design-Practices>

High Speed Digital Design, 1st Edition | Hanqiao -

High Speed Digital Design, Steven Krooswyk, these disciplines seeking knowledge and guidance in the design of high speed signaling and interconnects.

<http://store.elsevier.com/High-Speed-Digital-Design/Hanqiao-Zhang/isbn-9780124186637/>

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EMI/EMC Analysis for High-Speed Digital Design -

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Digital integrated circuits: a design perspective -

Digital integrated circuits: a design Design issues in low-voltage high-speed current-mode Ted Vucurevich, Analog and Digital Circuit Design in 65

<http://dl.acm.org/citation.cfm?id=227375>

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<http://www.kutenk.com/2010/05/semiconductor-modeling-for-simulating-signal-power-and-electromagnetic-integrity/>

Handbook of Digital Techniques for High- Speed -

Handbook of Digital Techniques for High-Speed Design: Design Examples, Signaling and validation of board-to-board 5-to-10 Gbps interconnects and Steven Woo at

<http://www.amazon.ca/Handbook-Digital-Techniques-High-Speed-Design/dp/013142291X>

Advanced Signal Integrity for High- Speed Digital -

HIGH-SPEED DIGITAL DESIGN. Impairment-Aware Analog Circuit Design. On-Chip Surfing Interconnect ; by Frank J. Fabozzi and Steven V. Mann

<http://www.kutenk.com/2009/11/advanced-signal-integrity-for-high-speed-digital-designs/>

Digital Design - Engineering Books - -

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<http://engineering-books.org/electrical-engineering/digital-design.php>

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