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By Yogesh Singh Chauhan;Darsen Duane Lu;Vanugopalan Sriramkumar

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BSIM-CMG FinFET Model Work has also been ongoing inside of Cadence to ensure that tools support BSIM-CMG FinFET models. Cadence simulation, 3D-IC, and 16nm
http://community.cadence.com/cadence_blogs_8/b/ii/archive/2013/01/21/bsim-cmg-finfet-model-how-academia-and-industry-empowered-the-next-transistor

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Darsen D. Lu is the author of Finfet Modeling for IC Simulation and Design (0.0 avg rating, 0 ratings, 0 reviews, published 2014)

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