

FinFET Modeling For IC Simulation And Design: Using The BSIM-CMG Standard By Yogesh Singh Chauhan;Darsen Duane Lu;Vanugopalan Sriramkumar

By Yogesh Singh Chauhan;Darsen Duane Lu;Vanugopalan Sriramkumar

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<http://news.synopsys.com/2015-04-06-TSMC-Certifies-Synopsys-Design-Tools-for-16-nm-FinFET-Plus-Production-and-for-10-nm-Early-Design-Starts>

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new simulation approaches are being used to model thermal of an IC requires modeling the heat modeling of the dissipated heat for FinFET

<http://semiengineering.com/finfet-reliability-issues/>

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<http://news.synopsys.com/index.php?item=123511>

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BSIM-CMG FinFET Model - Cadence Community -

BSIM-CMG FinFET Model Work has also been ongoing inside of Cadence to ensure that tools support BSIM-CMG FinFET models. Cadence simulation, 3D-IC, and 16nm

http://community.cadence.com/cadence_blogs_8/b/ii/archive/2013/01/21/bsim-cmg-finfet-model-how-academia-and-industry-empowered-the-next-transistor

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