

# **FinFET Modeling For IC Simulation And Design: Using The BSIM-CMG Standard By Yogesh Singh Chauhan;Darsen Duane Lu;Vanugopalan Sriramkumar**

**By Yogesh Singh Chauhan;Darsen Duane Lu;Vanugopalan Sriramkumar**

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Yogesh Singh Chauhan is the author of Finfet Modeling for IC Simulation and Design (0.0 avg rating, 0 ratings, 0 reviews, published 2014)

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This book is the first to explain FinFET modeling for IC simulation and the industry standard for FinFET modeling BSIM-CMG. Darsen D Lu; Yogesh Singh Chauhan;

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BSIM-CMG FinFET Model Work has also been ongoing inside of Cadence to ensure that tools support BSIM-CMG FinFET models. Cadence simulation, 3D-IC, and 16nm

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FinFET Modeling for IC Simulation and Design. Using the BSIM-CMG Standard. Chapter 7 Parasitic resistances and capacitances Available online 17 March 2015

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10-nm technology requirements in Synopsys' IC and 3-D FinFET modeling ; delivery of accurate circuit simulation results for the latest FinFET