

Dry Etching Technology For Semiconductors By Kazuo Nojiri

By Kazuo Nojiri

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<http://avxsearch.se/?q=%20Etching%20>

Dry etching process for semiconductor: US5888414 Micron Technology, Inc. Semiconductor processing method of making electrical Kazuo Takata: Etching method and

<http://www.google.com/patents/US4855017>

Dry etch processes, Ion Beam Etching, Plasma Etching, Reactive Ion Etching

<http://www.halbleiter.org/en/dryetching/etchprocesses/>

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Etching media and technology ("dry"). Each of these exists in several varieties. Wet etching Semiconductor technology;

[http://en.wikipedia.org/wiki/Etching_\(microfabrication\)](http://en.wikipedia.org/wiki/Etching_(microfabrication))

References from the article Developments of Plasma Etching Technology for Fabricating Semiconductor Dry Etching Technology Nojiri 1999 Proc. 21st Symp. Dry

<http://iopscience.iop.org/1347-4065/47/3R/1435/refs>

AG Semiconductor provides Used, Available technologies consist of physical vapor deposition both wet etching and dry etching such as reactive ion etch

<http://www.agsemiconductor.com/used-etch-equipment>

Kazuo Nojiri is the author of Dry Etching Technology for Semiconductors (0.0 avg rating, 0 ratings, 0 reviews, published 2014)

http://www.goodreads.com/author/show/8407761.Kazuo_Nojiri

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Nanoplas, a global supplier of plasma processing equipment to the semiconductor industry, announces a new dry-etch process offering etch selectivity for removing

<http://www.cemag.us/news/2013/03/nanoplas-develops-dry-etching-technology-semiconductors>

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This is an essential resource for semiconductor engineers and portable device designers. 3D IC Stacking Technology ratio silicon etch for TSV Dielectric

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http://link.springer.com/chapter/10.1007/978-3-319-10295-5_1

Shigeru Kakuta, Kazuo Nojiri, Kazue Takahashi: Assegnatario originale: including dry etching technology using Plasma dry cleaning of semiconductor

<http://www.google.it/patents/US6186153>

Kazuo Nojiri , Yoshinao Kawasaki Dry-etching apparatus: Integrated dry-wet processing apparatus and method for removing material on semiconductor wafers

<http://www.google.com/patents/US5952245>

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List of filed applications and granted patents for UNO SHOICHI. Authors: YUNOGAMI TAKASHI, NOJIRI KAZUO, MAEKAWA ATSUSHI, UNO SHOICHI, a plasma dry etching

<http://www.patentsobserver.com/public/author/show.action?toShow=UNO+SHOICHI&company=Hitachi+Ltd>

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Books: Physics: Electromagnetism: Semiconductor Physics. Series on Semiconductor Science & Technology); Kazuo Nojiri (2014) Dry Etching Technology for

<http://www.researchbooks.org/Books/Physics/Electromagnetism/Semiconductor-Physics/>

Kazuo Nojiri is a CTO of Lam Research Japan. He has 37 years of experience in semiconductor industry. Prior to joining Lam in 2000, he worked for Hitachi Ltd. for 25

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