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<http://www.agsemiconductor.com/used-etch-equipment>

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Science & Technology Dry Etching Technology for Half-Micron Patterning Fine patterning technology in semiconductor manufacturing has made

<http://www.dtic.mil/cgi-bin/GetTRDoc?AD=ADA359105>

Developments of Plasma Etching Technology for -

References from the article Developments of Plasma Etching Technology for Fabricating Semiconductor Dry Etching Technology Nojiri 1999 Proc. 21st Symp. Dry

<http://iopscience.iop.org/1347-4065/47/3R/1435/refs>

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Dry Etching Technology for Semiconductors [Kazuo Nojiri] on Amazon.com. *FREE* shipping on qualifying offers. This book is a must-have reference to dry etching

<http://www.amazon.com/Etching-Technology-Semiconductors-Kazuo-Nojiri/dp/331910294X>

Etching: A crucial step in semiconductor -

A crucial step in semiconductor Dry or wet, etching technology will be the subject will summarize a SEMICON West Semiconductor Technology Symposium

<http://electroiq.com/blog/2015/07/etching-a-crucial-step-in-semiconductor-manufacturing/>

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Inbunden, 2014. Pris 920 kr. K p Dry Etching Technology for Semiconductors (9783319102948) av Kazuo Nojiri p Bokus.com

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Etchings - AbeBooks -

Technique of Etching by Lalanne, Dry Etching Technology for Semiconductors. Nojiri, Kazuo. The Complete Etchings Lithographs and Monotypes.

<http://www.abebooks.co.uk/book-search/kw/etchings/>

Nanoplas Develops Dry-Etching Technology for -

Nanoplas, a global supplier of plasma processing equipment to the semiconductor industry, announces a new dry-etch process offering etch selectivity for removing

<http://www.cemag.us/news/2013/03/nanoplas-develops-dry-etching-technology-semiconductors>

Kazuo Nojiri (Author of Dry Etching Technology -

Kazuo Nojiri is the author of Dry Etching Technology for Semiconductors (0.0 avg rating, 0 ratings, 0 reviews, published 2014)

http://www.goodreads.com/author/show/8407761.Kazuo_Nojiri

UNO SHOICHI Filed Applications and Granted Patents -

List of filed applications and granted patents for UNO SHOICHI. Authors: YUNOGAMI TAKASHI, NOJIRI KAZUO, MAEKAWA ATSUSHI, UNO SHOICHI, a plasma dry etching

<http://www.patentsobserver.com/public/author/show.action?toShow=UNO+SHOICHI&company=Hitachi+Ltd>

Patent US4855017 - Trench etch process for a -

Dry etching process for semiconductor: US5888414 Micron Technology, Inc. Semiconductor processing method of making electrical Kazuo Takata: Etching method and

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%0 Journal Article %A Nojiri, Kazuo %A Tsunokuni, Kazuyuki %T Study of gate oxide breakdown caused by charge buildup during dry etching %D 1993 %J Journal of Vacuum <http://scitation.aip.org/content/avs/journal/jvstb/11/5/10.1116/1.586484/cite/endnote>

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Study of gate oxide breakdown caused by charge buildup during dry etching: Authors: Nojiri, Kazuo; Journal of Vacuum Science & Technology B:

<http://adsabs.harvard.edu/abs/1993JVSTB..11.1819N>

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Mashimo S and Tsukada T 1993 Proc. Symp. on High Selective Dry Etching and Nojiri K and Tsunokuni K 1993 Pulsed plasma etching for semiconductor

<http://iopscience.iop.org/0963-0252/5/2/010/refs>

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Dry etching - Wikipedia, the free encyclopedia -

Dry etching refers to the removal of Dry etching is currently used in semiconductor fabrication processes due to its unique ability over wet etch to do

http://en.wikipedia.org/wiki/Dry_etching

Dry Etching Technology for Semiconductors | Kazuo -

Kazuo Nojiri is a CTO of Lam Research Japan. He has 37 years of experience in semiconductor industry. Prior to joining Lam in 2000, he worked for Hitachi Ltd. for 25

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